

apparatus; and

fabricating the integrated circuit.

18. An integrated circuit, fabricated using method
5 comprising:

sending a verification result of a circuit design to an apparatus for circuit design verification through a computer network;

10 instructing the apparatus to convert the verification result to a pre-analysis indication file and store the pre-analysis indication file in an indication file storage portion;

15 sending analysis information about a redundant non-active portion of the circuit design to the apparatus through the computer network;

instructing the apparatus to exclude an affect of the redundant non-active portion from the verification result and make an analysis indication file;

20 instructing the apparatus to store the analysis indication file in the indication file storage portion;

receiving the analysis indication file from the apparatus; and

fabricating the integrated circuit.